## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Kazuo MATSUZAKI, et al.

Group Art Unit: 2811

Serial No.: 09/756,686

Examiner: S. Loke

Filed: January 9, 2001

Attorney Docket No.: FUJI:179

For:

SEMICONDUCTOR DEVICE EXHIBITING A HIGH BREAKDOWN VOLTAGE AND THE METHOD

OF MANUFACTURING THE SAME

COMMISSIONER FOR PATENTS P.O. Box 1450 ALEXANDRIA, VA 22313-1450

Certificate of Mailing

I hereby certify that this paper is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on:

Marc A. Rossi

## **BOX-AF** & REQUEST FOR RECONSIDERATION AMENDMENT AFTER FINAL

In reply to the Final rejection dated February 21, 2003, this application has been amended Sir: as indicated below. A marked up version of the changes is attached. No fee is due.

## IN THE DRAWINGS:

Enclosed for the examiner's approval are copies of Figs. 13, 14a, 14b, and 21-24(b) with red ink markings showing the proposed changes, namely correcting reference --159a-- in Fig. 1, formatting Figs. 14a, 14b as a single figure (same change to Figs. 24a, 24b), and including the legend "Prior Art" in Figs. 21, 22a, 22b, 23, and 24.